

Amendments to the Claims

Please amend claims 1 and 11 as shown in the following listing of claims. This listing of claims will replace all prior versions, and listings, of claims in the application.

1 1. (currently amended) A display device with pixels arranged in columns and
2 rows, in which the pixels of a row can be selected by means of a row voltage
3 supplied via control lines, and column voltages that correspond to the image data
4 of the selected pixel to be displayed can be supplied via data lines, wherein
5 mutually adjoining pixel groups arranged in a row or column, consisting of
6 adjoining pixels of a row or column, are connected to adjoining control lines or
7 data lines, as applicable, in alternation, some of the control lines being connected
8 to a plurality of delay units such that only every other control line is connected to
9 a particular delay unit of the delay units and each of remaining control lines is not
10 connected to any delay unit, the delay units being used to store row voltage values
11 for the control lines connected to the delay units until a clock signal is supplied to
12 the delay units.

1 2. (previously presented) A display device as claimed in claim 1,
2 characterized in that a pixel group comprises one pixel.

1 3. (previously presented) A display device as claimed in claim 1,
2 characterized in that mutually adjoining pixels of one row are alternately
3 connected to the adjoining control lines.

1 4. (canceled).

1 5. (canceled).

1 6. (canceled).

- 1 7. (previously presented) A display device as claimed in claim 1,
2 characterized in that the pixels comprise switching elements with control
3 terminals which are connected to the control lines and data terminals which are
4 connected to the data lines.
- 1 8. (previously presented) A display device as claimed in claim 1,
2 characterized in that the rows and columns situated at the edges of the display
3 device are covered.
- 1 9. (previously presented) A method of controlling a display device as claimed
2 in claim 1.
- 1 10. (previously presented) A display device as claimed in claim 1, wherein the
2 delay units are D-flip-flops.
- 1 11. (currently amended) A display device with pixels arranged in columns and
2 rows, in which the pixels of a row can be selected by means of a row voltage
3 supplied via control lines, and column voltages that correspond to the image data
4 of the selected pixel to be displayed can be supplied via data lines, wherein
5 mutually adjoining pixel groups arranged in a row or column, consisting of
6 adjoining pixels of a row or column, are connected to adjoining control lines or
7 data lines, as applicable, in alternation, some of the data lines being connected to a
8 plurality of delay units such that only every other data line is connected to a
9 particular delay unit of the delay units and each of remaining control lines is not
10 connected to any delay unit, the delay units being used to store column voltage
11 values for the data lines connected to the delay units until a clock signal is
12 supplied to the delay units.
- 1 12. (previously presented) A display device as claimed in claim 11, wherein
2 the delay units are D-flip-flops.
- 1 13. (previously presented) A display device as claimed in claim 11,
2 characterized in that a pixel group comprises one pixel.

1 14. (previously presented) A display device as claimed in claim 11,
2 characterized in that mutually adjoining pixels of a column are connected to the
3 adjoining data lines in alternation.

1 15. (previously presented) A display device as claimed in claim 11,
2 characterized in that the pixels comprise switching elements with control
3 terminals which are connected to the control lines and data terminals which are
4 connected to the data lines.

1 16. (previously presented) A display device as claimed in claim 11,
2 characterized in that the rows and columns situated at the edges of the display
3 device are covered.

1 17. (previously presented) A method of controlling a display device as claimed
2 in claim 11.